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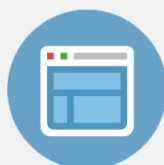
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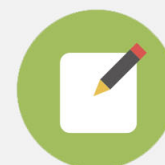


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Analysis of lateral transport through the inversion layer in amorphous silicon/crystalline silicon heterojunction solar cells

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In amorphous/crystalline silicon heterojunction solar cells, an inversion layer is present at the front interface. By combining numerical simulations and experiments, we examine the contribution of the inversion layer to lateral transport and assess whether this layer can be exploited to replace the front transparent conductive oxide (TCO) in devices. For this, heterojunction solar cells of different areas (2×2 , 4×4 , and 6×6 mm²) with and without TCO layers on the front side were prepared. Laser-beam-induced current measurements are compared with simulation results from the ASPIN2 semiconductor simulator. Current collection is constant across millimeter distances for cells with TCO; however, carriers traveling more than a few hundred microns in cells without TCO recombine before they can be collected. Simulations show that increasing the valence band offset increases the concentration of holes under the surface of n-type crystalline silicon, which increases the conductivity of the inversion layer. Unfortunately, this also impedes transport across the barrier to the emitter. We conclude that the lateral conductivity of the inversion layer may not suffice to fully replace the front TCO in heterojunction devices. © 2013 AIP Publishing LLC.

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I. INTRODUCTION

Hydrogenated amorphous silicon (a-Si:H)/crystalline silicon (c-Si) heterojunction solar cells are promising candidates for high-efficiency low-cost solar cells. With stacks of intrinsic and doped a-Si:H layers, excellent surface passivation can be achieved, leading to high open-circuit voltages in excess of 720 mV.^{1,2} The drawback of the amorphous emitter is its low mobility, which, together with a thickness limited to a few nanometers to reduce light absorption losses,^{3,4} results in a high resistivity. To overcome this disadvantage and achieve high efficiencies, a transparent conductive oxide (TCO) layer is required on top of the emitter for lateral transport of photogenerated charge carriers to metal finger contacts; the TCO also serves as a single-layer antireflection coating. To reduce optical losses associated with free-carrier absorption in TCOs,⁵ new high-mobility materials such as hydrogenated indium oxide are being introduced.^{6–8}

Another feature of the a-Si:H emitter is that it forms a heterojunction with c-Si due to their different bandgaps and electron affinities. As a consequence of the band discontinuity at the heterojunction, band bending in c-Si is increased compared to a homojunction and the hole concentration at the surface of the n-type c-Si exceeds the concentration of majority electrons in the bulk, thus forming an inversion layer (inset in Figure 5).

Recently, there has been experimental confirmation of the existence of the inversion layer using capacitive methods,^{9,10} planar conductivity measurements,^{11–13} and conductive probe atomic force microscopy.^{14,15} Until now, the

inversion layer has been considered only as a tool to characterize the band offsets at the a-Si:H/c-Si interface^{16,17} and its influence on lateral transport in operating devices has not yet been analyzed. Lateral transport through an inversion layer is successfully leveraged in modulation-doped field-effect transistors¹⁸ and in metal-insulator-conductor inversion layer solar cells.^{19–23} This prompted us to examine whether this layer can be used for lateral transport in heterojunction solar cells, with the specific aim of assessing TCO-free heterojunction solar cells. Such cells would feature an improved blue and red response, be potentially cheaper, and avoid passivation layer damage related to TCO sputtering,²⁴ compared to our standard devices.²⁵ When the TCO is absent, the a-Si:H emitter is too resistive to contribute noticeably to the lateral conductivity and has the same role as the insulator in metal-insulator-semiconductor solar cells of inducing an inversion layer in c-Si. However, while in metal-insulator-semiconductor solar cells, the inversion layer is induced by fixed surface charges, the inversion layer in a-Si:H/c-Si solar cells is a consequence of the a-Si:H emitter doping and the band discontinuities at the interface, introducing new parameters to alter the conductivity of the inversion layer.

A schematic representation of the TCO-free concept is presented in Figure 1, highlighting the difference between devices with a TCO layer between the metal fingers (Figure 1(a)) and devices without such layers (Figure 1(b)). The hole mobility in the inversion layer is lower than in bulk c-Si due to carrier surface scattering,²⁶ but it is still higher than in TCOs. Consequently, for the same specific resistance, a lower concentration of charge carriers in the channel is needed, reducing free-carrier absorption.

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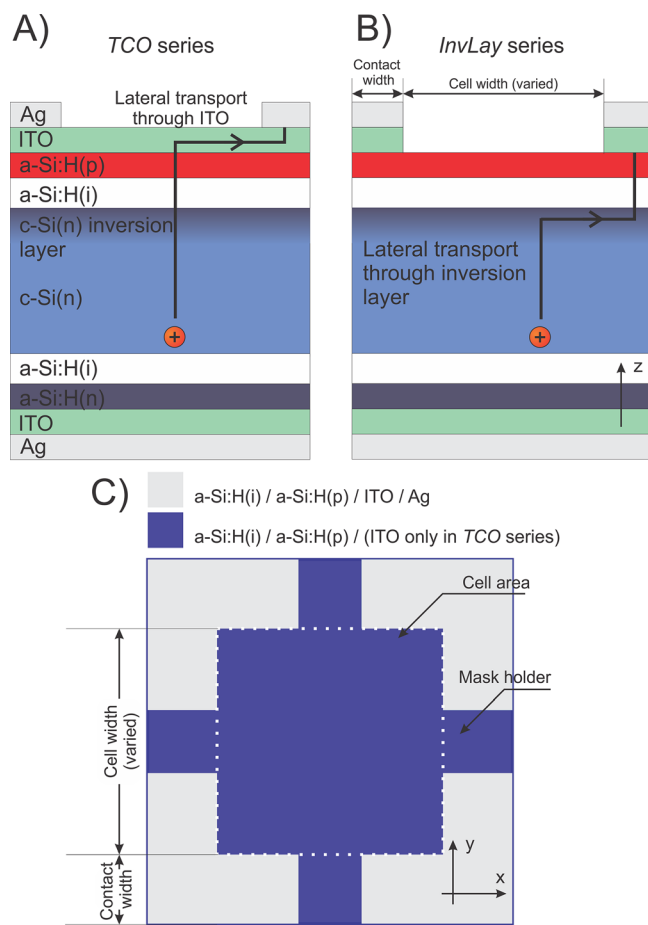


FIG. 1. (a) Schematic of a solar cell in the *TCO* series, with lateral transport of collected photogenerated holes through the ITO layer. (b) Schematic of a solar cell in the *InvLay* series, with lateral transport through the inversion layer. (c) Schematic of finished cells (top view).

In this paper, we employ numerical simulations as well as experiments to examine the contribution of the inversion layer to the lateral transport of photogenerated carriers on their way to the front external contact. Laser-beam-induced current (LBIC) maps of specially designed heterojunction solar cells with and without TCO layers of various areas were measured. *ASPIN2*, a two-dimensional solar cell simulator,²⁷ was used to simulate one-dimensional LBIC profiles of these cells and evaluate how different parameters (i.e., band discontinuities) impact the lateral transport.

II. EXPERIMENTAL AND SIMULATIONS

As bases to make the cells, 3 Ωcm , n-type, (100) polished, 280 μm -thick, float-zone wafers were used onto which intrinsic and doped a-Si:H layers were deposited by plasma-enhanced chemical vapor deposition (PECVD).²⁸ Indium tin oxide (ITO) was sputtered⁵ and silver was evaporated over the entire back side of each cell. Laser-cut silicon shadow masks were aligned by eye to cover the cell area during deposition of the front contact, defining cells with areas of 2×2 , 4×4 , and 6×6 mm^2 , as shown in Figure 1(c). Two different series of cells were made: The “TCO” series (Figure 1(a)) had ITO sputtered over the entire front side and only silver evaporated through

the mask, while the “InvLay” series (Figure 1(b)) had both ITO sputtered and silver evaporated through the mask. *InvLay* series cells thus do not have an ITO layer on the front of the active cell area. The ITO layer was present under the silver contacts in both cases to ensure good-quality electrical contacts.⁵ For the *InvLay* series, the masks were aligned twice, first for ITO sputtering and second for silver evaporation. Each series was co-deposited on the same wafer, which was cut in half before processing. Due to masking, the device structure has four front contacts, one in each corner, which were all electrically connected with contacting probes to form one front contact.

LBIC was measured by using an apparatus developed at the University of Ljubljana.²⁹ A laser diode with a wavelength of 638 nm was used as a light source. A laser diode controller and waveform generator were used to drive the 49-Hz sine waveform current through the diode. A beam splitter split the collimated beam in half; half the beam illuminated the measured cell and half illuminated a reference detector, which was used to compensate variations in the laser diode intensity. The beam illuminating the cell was passed through a focusing lens, giving a knife-edge beam width of 85 μm . The power of the beam illuminating the cell was 6.3 μW , as measured with a reference detector. This power was also used as the input beam power for LBIC simulations. The cell current was passed through a 10 Ω resistor, which set the operating point sufficiently close to short-circuit conditions. The amplitude of the voltage drop across the resistor was measured with a lock-in amplifier that responded only to the modulated laser beam. Linear stage controllers moved the measured cell in steps of 75 μm , giving a two-dimensional map of the cell’s short-circuit current response.

ASPIN2, a numerical solar cell simulation software capable of simulating cell structures in two dimensions, was used to calculate one-dimensional LBIC profiles. The simulation structures are shown in Figures 1(a) and 1(b). To simulate surface states, a 1-nm-thick defect layer was introduced on both sides of the c-Si absorber with a volume defect density of 10^{14} cm^{-3} , corresponding to a surface defect density of 10^7 cm^{-2} . With the data for c-Si from Table I, the surface defect density (neglecting the field-effect passivation) results in a surface recombination velocity of 1 cm/s and an effective lifetime of 3.7 ms, consistent with regularly measured lifetimes.²⁵

ITO was modeled as a conductor that contributed only to the series resistance; we neglected other effects like band bending and band-to-band tunneling.³⁰ The thickness of the ITO was 65 nm and its mobility and free-carrier concentration are 30 $\text{cm}^2/(\text{Vs})$ and $3 \times 10^{20} \text{ cm}^{-3}$, respectively, giving it a sheet resistance of 107 Ω/sq . Generation rate profiles were calculated with the *SunShine* optical simulator,³¹ since more advanced features like ray tracing³² were not required for the polished wafers. Tunneling of holes through the a-Si:H/c-Si barrier was not included in the simulations. The input parameters used in the simulations are presented in Table I. Thicknesses of the layers were measured, while other parameters were taken from the literature.

TABLE I. Simulation input parameters. The thermal velocity was 10^7 cm/s for all simulations.

Parameter	a-Si:H(p)	a-Si:H(i)	a-Si:H(n)	c-Si
Thickness (nm)	8	4	10	280 000
Bandgap (eV)	1.7	1.7	1.7	1.124
Electron affinity (eV)	3.9	3.9	3.9	4.05
Acceptor/donor doping concentration (cm^{-3})	$9 \times 10^{18}/0$	0/0	$0/1 \times 10^{19}$	$0/1.5 \times 10^{15}$
Electron/hole mobility ($\text{cm}^2/(\text{Vs})$)	10/2	10/2	10/2	1300/450
Effective DOS in CB/VB (cm^{-3})	$2.00 \times 10^{20}/2.00 \times 10^{20}$	$2.00 \times 10^{20}/2.00 \times 10^{20}$	$2.00 \times 10^{20}/2.00 \times 10^{20}$	$2.80 \times 10^{19}/2.65 \times 10^{19}$
Relative permittivity	11.9	11.9	11.9	11.9
CB tail states				
Total states (cm^{-3})	3×10^{18}	3×10^{18}	3×10^{18}	...
Characteristic energy (eV)	0.03	0.03	0.03	...
e/h capture cross-section (cm^{-2})	$10^{-16}/10^{-15}$	$10^{-16}/10^{-15}$	$10^{-16}/10^{-15}$...
VB tail states				
Total states (cm^{-3})	5×10^{18}	5×10^{18}	5×10^{18}	...
Characteristic energy (eV)	0.05	0.05	0.05	...
e/h capture cross-section (cm^{-2})	$10^{-15}/10^{-16}$	$10^{-15}/10^{-16}$	$10^{-15}/10^{-16}$...
Dangling bond states				
Total states (cm^{-3})	10^{18}	10^{16}	10^{18}	...
E_{+0}/E_{0-} (eV)	0.8/1	0.7/0.9	0.5/0.7	...
σ (eV)	0.22	0.22	0.22	...
e/h neutral capture cross-section (cm^{-2})	$10^{-14}/10^{-14}$	$10^{-14}/10^{-14}$	$10^{-14}/10^{-14}$...
e/h charged capture cross-section (cm^{-2})	$10^{-15}/10^{-15}$	$10^{-15}/10^{-15}$	$10^{-15}/10^{-15}$...
Correlation energy (eV)	0.2	0.2	0.2	...
Midgap states				
Total states (cm^{-3})	2×10^9
e/h capture cross-section (cm^{-2})	$10^{-14}/10^{-14}$...

III. RESULTS AND DISCUSSION

First, we compare the free-carrier absorption of an optimized front ITO layer^{3,5} and an equivalent-resistance c-Si inversion layer to evaluate the potential short-circuit current gain in a TCO-free heterojunction solar cell. We chose a wavelength of 1000 nm where c-Si is still absorbing and free-carrier absorption begins to occur. At this wavelength, the absorption coefficient of our ITO, measured by ellipsometry,⁵ is 5000 cm^{-1} . To get an estimate of the inversion layer absorption coefficient, we follow Schroder³³

$$\alpha_{\text{FCA}} = \frac{q^3 \lambda^2 p}{4\pi^2 \epsilon_0 c^3 n_r m^* \mu}, \quad (1)$$

where λ is wavelength, p is hole concentration, n_r is refractive index, m^* is effective mass, μ is mobility, and other constants have their usual meanings. To compare the absorption coefficients, we assume that the inversion layer has the same thickness and sheet resistance as the ITO (65 nm and $107 \text{ } \Omega/\text{sq}$, respectively). Such a low sheet resistance of the inversion layer is unlikely to be obtained in practice (see Figures 5 and 6 and the corresponding discussion), and serves here only for the comparison of free-carrier absorption in the inversion layer and ITO. Supposing a low hole mobility at the surface of c-Si of $150 \text{ cm}^2/(\text{Vs})$, we get a hole concentration of $6 \times 10^{19} \text{ cm}^{-3}$, which together with an effective mass of $0.37 \times m_0$ and refractive index of 3.57 gives a c-Si inversion layer absorption coefficient of 64 cm^{-1} , 78 times lower than that of the ITO. Based on previous experimental results in which the free-carrier density of the front ITO layer was

varied in silicon heterojunction solar cells,⁵ we estimate that this would correspond to a gain of more than $0.5 \text{ mA}/\text{cm}^2$.

An LBIC scan measured in the dark is shown in Figure 2 for the $6 \times 6 \text{ cm}^2$ TCO cell. Smaller area cells are not shown, since they have similar LBIC response. Cells in the TCO series show almost no variation in LBIC response over the entire area, as expected, indicating that the conductivity of the ITO layers is high enough not to hinder lateral

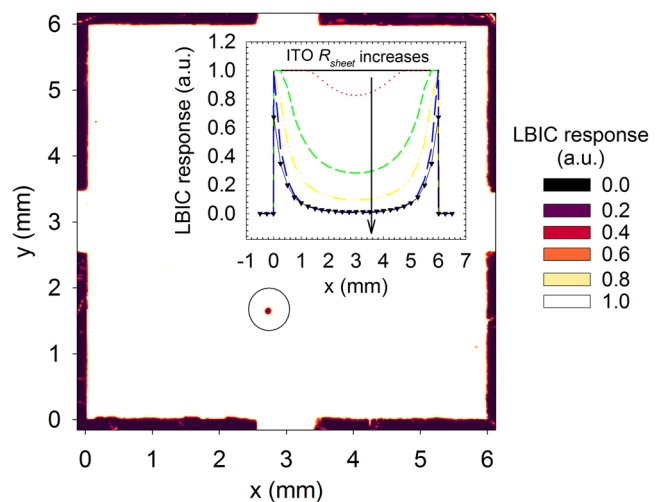


FIG. 2. LBIC scan of the $6 \times 6 \text{ cm}^2$ TCO cell that has full ITO coverage. The circle marks a defect. The inset graph shows simulated one-dimensional LBIC profiles when increasing the ITO sheet resistance (values are 100, 300, 1 k, 3 k, and 1 M Ω/sq). The lowest line with symbols shows the simulated profile without an ITO layer.

photogenerated carrier collection. Increasing the sheet resistance from $100 \Omega/\text{sq}$ to $3 \text{ k}\Omega/\text{sq}$ results in a drop of the LBIC profiles, as shown in the inset of Figure 2. For a very high resistance value of $1 \text{ M}\Omega/\text{sq}$, the profile approaches the simulation profile without an ITO layer (line with symbols in the inset). Note that the sheet resistance of the doped a-Si:H emitter layer is $400 \text{ k}\Omega/\text{sq}$. The difference close to the contacts is a consequence of the antireflection provided by the ITO.

An LBIC scan measured in the dark is shown in Figure 3 for the *InvLay* series. Cells in the *InvLay* series have high LBIC response only near the contacts, indicating that lateral transport is poor without the ITO layer. The inversion layer is apparently insufficiently conductive to transport holes across millimeter distances without significant recombination. Inspection of the cells under a microscope shows that the higher response along the top and left edges is a consequence of imperfect alignment of the deposition masks, as some ITO was not covered by evaporated silver (arrows in Figure 3).

Analysis of the *TCO* series shows that the effective defect area revealed in the LBIC scans is on average smaller than for the *InvLay* series. The exact origin of these anomalies during cell fabrication is not known. Some of the anomalies are marked with circles in Figures 2 and 3. The ITO layer reduces the area of an imperfection's influence, so its role is not only to provide lateral transport but also to facilitate collection of carriers near the defective regions.

Measured one-dimensional LBIC profiles in the dark are compared with simulated values in Figure 4. For each two-dimensional LBIC scan, one vertical and one horizontal profile were chosen. The vertical profiles were chosen closer to the middle of the cell than the horizontal profiles, as indicated by the white dashed lines in Figure 3, showing how the distance from the contacts perpendicular to a profile affects that profile. LBIC profiles are centered at the middle of the cell for a clearer plot. The measured profiles reach an asymmetric maximum because of the misalignment of the mask. The simulated profiles for the smaller 2×2 and $4 \times 4 \text{ mm}^2$ cells show deeper drops in response at the center of the cells

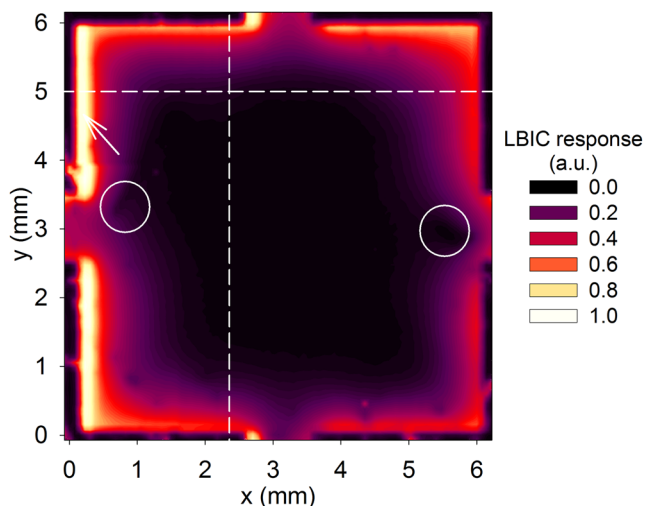


FIG. 3. LBIC scan of the $6 \times 6 \text{ cm}^2$ *InvLay* cell, which has no ITO on the active cell area. Dashed lines show the location of the widest 6 cm profile in Figure 4. Arrows point to areas covered with ITO but not silver due to slight misalignment of the mask. Circles mark defects.

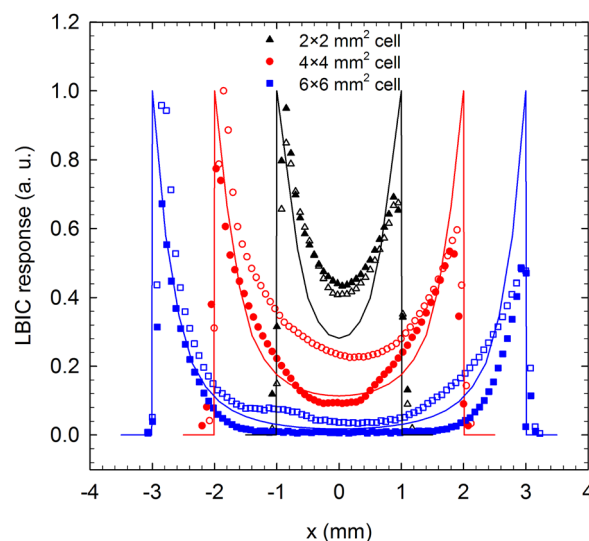


FIG. 4. Measured and simulated LBIC profiles. Full symbols and empty symbols are the vertical and horizontal profiles marked by dashed lines in Figure 3, respectively. Lines represent simulated LBIC profiles.

compared to the measurements, since in the two-dimensional simulation, contacts are only on the sides and not all around the cell like in the measurements. Nevertheless, the simulations faithfully reproduce the shape and approximate values of the measured LBIC profiles, and thus allow us to explore the effect on inversion layer transport of changing parameters that are at present difficult to control experimentally.

The simulated and measured LBIC response at the middle of the cells is unacceptably low (if the inversion layer is to replace the front TCO), but rises with decreasing cell area. To extract more current from the *InvLay* series of cells, the contacts have to be closer together or the conductivity of the inversion layer needs to be increased. Simulations indicate that for the LBIC profile to drop by less than 5% of its maximum value the contact spacing should be less than 0.75 mm . Assuming a metal finger width of $75 \mu\text{m}$, almost 10% of the cell area

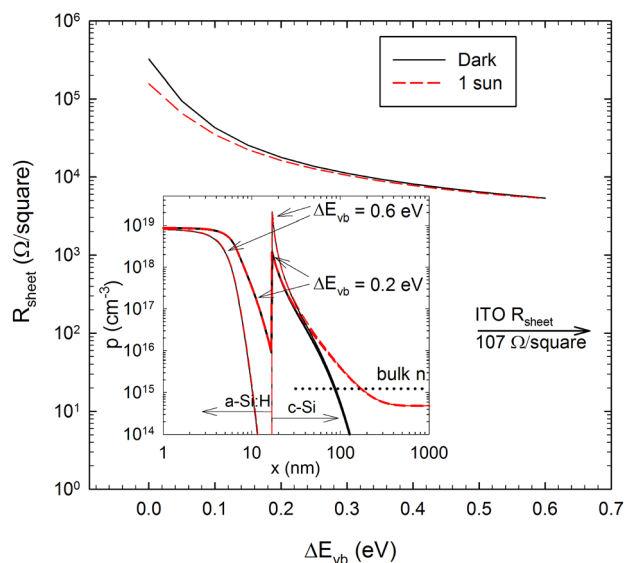


FIG. 5. Inversion layer sheet resistance dependence on ΔE_{vb} . Inset shows the hole concentration profile near the junction (between the contacts) for two different ΔE_{vb} .

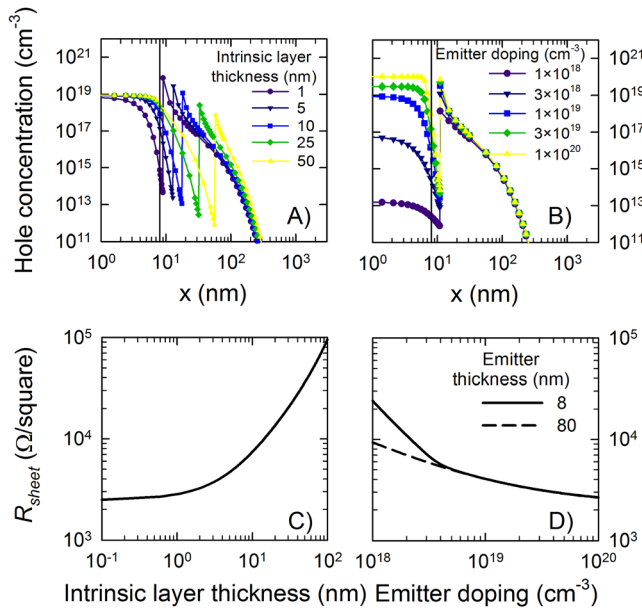


FIG. 6. Hole concentration profiles for several intrinsic layer thicknesses (a) and emitter doping concentrations (b). The black vertical lines indicate the interface of the doped and intrinsic a-Si:H layers. The c-Si starts at the discontinuous jump in hole concentration. Inversion layer sheet resistance as a function of intrinsic layer thickness (c) and emitter doping concentration (d).

would be shaded by the metallic front grid. This leaves inversion layer conductivity increase as the only viable option for lateral transport through the inversion layer.

In order to increase the conductivity of the inversion layer, more carriers need to be present at the surface of the c-Si absorber. This can be achieved for n-type c-Si by a higher valence band offset (ΔE_{vb}) at the a-Si:H/c-Si heterojunction, which increases the barrier height but also increases the band bending and hole concentration in c-Si. The driving force due to increased ΔE_{vb} , which pulls holes from the p-type a-Si:H emitter into the inversion layer, acting against the built-in electric field, is not sufficient to create a highly conductive inversion layer. To effectively increase the hole concentration in the inversion layer, both a large enough driving force and a suitably large amount of holes in the p-type a-Si:H emitter are needed, demanding a high ΔE_{vb} and a sufficiently doped and thick emitter.¹⁸ In practice, the ΔE_{vb} can be increased by 0.15 eV by increasing the a-Si:H hydrogen content from 12% to 24%.³⁴ Another possibility is to deposit hydrogenated amorphous silicon suboxide films (a-SiO_x:H) with higher bandgaps than a-Si:H.^{35,36} In the simulations, the initial ΔE_{vb} of 0.43 eV was varied by hypothetically changing the front a-Si:H layers' bandgap, while leaving all other parameters the same. Note that widening the bandgap of the emitter and front intrinsic a-Si:H layers would also decrease the UV and visible parasitic absorption in these layers.³

The simulated sheet resistance of the inversion layer is shown in Figure 5. The sheet resistance was calculated according to the following equation:

$$R_{sh} = \frac{1}{q\mu_p \int_0^{d_{inv}} p \cdot dx}, \quad (2)$$

where q is the elementary charge, μ_p is the bulk hole mobility taken from Table I, p is the hole concentration, and d_{inv} is the thickness of the inversion layer. The hole concentration was averaged from the surface of c-Si to d_{inv} , which is defined as the thickness at which the hole concentration drops below the majority electron concentration in the bulk c-Si (marked by the dotted line in the inset of Figure 5). By integrating only to d_{inv} , the contribution of photogenerated holes in bulk c-Si is removed. Increasing ΔE_{vb} at the a-Si:H/c-Si interface increases the hole concentration at the surface of the c-Si, as can be seen in the inset of Figure 5. Favre *et al.*¹³ simulated the same structure without the intrinsic layer, which changes the band bending, but at room temperature our values of the surface hole concentration, obtained by evaluating the integral in Eq. (2) without illumination, have the same ΔE_{vb} trend. The dashed line shows the sheet resistance at 1-sun illumination, which is only slightly lower than in the dark. Thus, although our LBIC measurements were performed in the dark, the inversion layer will not become significantly more conductive when illuminated, as in an operating device. From the inset, it can be seen that when illumination is applied the peak concentration of holes at the c-Si surface increases negligibly, but the concentration at a depth of 100 nm increases by nearly an order of magnitude, which also increases d_{inv} . The illumination influence on holes in the a-Si:H layers is negligible. Note that the actual hole mobility in the inversion layer is lower than the assumed bulk value,²⁶ which additionally increases the inversion layer sheet resistance. Even in the best case, with $\Delta E_{vb} = 0.6$ eV and the mobility value taken from the bulk, the sheet resistance of the inversion layer is two orders of magnitude higher than the ITO sheet resistance.

The concentration of holes in the c-Si inversion layer is also affected by the thickness of the intrinsic layer and the doping concentration of the emitter. The plots in Figure 6 show how the intrinsic layer thickness and the emitter doping concentration influence the hole concentration profile and consequently the sheet resistance calculated according to Eq. (2). Since illumination only slightly alters the inversion layer sheet resistance, these simulations were done in the dark with data from Table I ($\Delta E_{vb} = 0.43$ eV). The density of surface defect states was kept constant, regardless of the intrinsic layer thickness. The intrinsic a-Si:H layer provides no additional free holes and increasing its thickness results in a lower hole concentration in the c-Si (Figure 6(a)). The peak hole concentration in the inversion layer decreases and moves further away from the doped a-Si:H layer. The sheet resistance of the inversion layer starts to significantly increase for intrinsic layer thicknesses greater than 3 nm (Figure 6(c)). For a constant intrinsic layer thickness of 4 nm, the depletion region in the emitter is narrower than the emitter for high doping concentration (above 3×10^{18} cm⁻³ for an 8-nm-thick emitter). In this case, lowering the doping concentration slightly reduces the hole concentration in the c-Si inversion layer (Figure 6(b)). Once the doping is low enough that the emitter depletion region extends across its entire thickness, the hole concentration is drastically lowered in the emitter as well as in the c-Si inversion layer. The sheet resistance of the inversion layer then exhibits a sharp

increase with decreasing doping concentration (Figure 6(d), full line). As long as the emitter is thicker than the depletion region, this sharp increase of sheet resistance is absent (Figure 6(c) dashed line). Even for the thinnest intrinsic layer and the highest emitter doping concentration, the sheet resistance of the inversion layer is still more than an order of magnitude higher than the sheet resistance of the ITO. As with varying ΔE_{vb} , thinning the intrinsic layer or increasing the doping of the emitter is insufficient to achieve a low inversion layer sheet resistance.

In addition to altering the inversion layer, changing the band offset or intrinsic layer thickness influences the hole collection. The effect of ΔE_{vb} on LBIC response was simulated and the results are shown in Figure 7 for the *InvLay* $2 \times 2 \text{ mm}^2$ cell. With increasing ΔE_{vb} from 0 to 0.5 eV, the LBIC profiles rise because of the decrease in the inversion layer sheet resistance, which enhances collection of photo-generated carriers from the middle of the cell. The LBIC profile responds similarly if the inversion layer sheet resistance is reduced by a thinner intrinsic layer or higher-doped emitter. After the offset exceeds 0.5 eV, however, a sharp decrease can be observed in both LBIC response and short-circuit current (inset in Figure 7). In this case, ΔE_{vb} is high enough that thermionic emission over the barrier becomes the limiting transport mechanism, impeding transport of holes across the barrier to the emitter region.

To circumvent this problem, a patterned structure was simulated in which the bandgap of the a-Si:H layers was kept constant under the metal contacts to allow for good transverse transport. Here, ΔE_{vb} was varied only under the active cell area. This structure could be made in practice by using shadow masks during PECVD depositions. With this structure, the barrier height under the contacts is kept low enough that thermionic emission does not become the limiting mechanism and hole collection is not impeded. LBIC simulations in Figure 8, and the short-circuit current in the inset, show an increase in collection with higher valence band offset, until

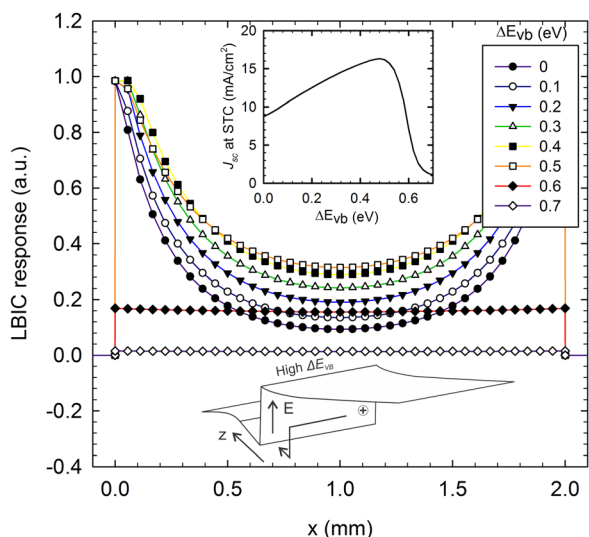


FIG. 7. Simulated effect of ΔE_{vb} on the dark LBIC profiles of the $2 \times 2 \text{ mm}^2$ *InvLay* cell. The inset shows the short-circuit current at 1 sun illumination. The schematic at the bottom illustrates lateral hole transport and thermionic emission over the barrier.

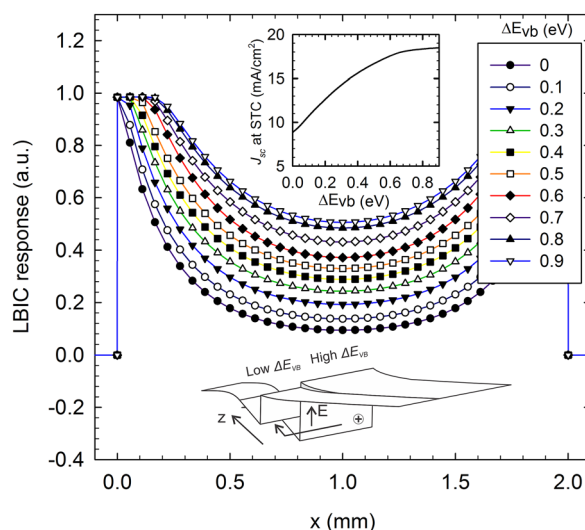


FIG. 8. Simulated effect of ΔE_{vb} on the dark LBIC profiles of the $2 \times 2 \text{ mm}^2$ *InvLay* cell for a patterned structure. The inset shows the short-circuit current at 1 sun illumination. The schematic at the bottom illustrates lateral hole transport and unobstructed collection under the contacts, where the barrier is lower.

saturation is reached. The Fermi level at the c-Si surface is in this case positioned very close to the valence band, and a further increase in charge concentration at the surface would require much higher band offsets as well as higher doping of the a-Si:H emitter. Even when this saturation occurs, the LBIC profile is not flat, as with an ITO layer.

Without patterning, the barrier height must be kept low enough not to inhibit hole collection under the contacts. The conductivity of the inversion layer cannot be varied much in this case and it, therefore, cannot replace the TCO layer. With patterning, the sheet resistance of the inversion layer can be decreased under the non-contacted area with more freedom. Nonetheless, Figure 8 reveals that even with patterned contacts and a large valence band offset of 0.9 eV, holes traveling more than approximately $200 \mu\text{m}$ are prone to recombine before collection. Simulation results indicate that to achieve a flat LBIC profile for a $2 \times 2 \text{ mm}^2$ cell, the acceptor doping in the a-Si:H(p) layer would have to be increased tenfold and the valence band offset would have to be higher than 2.6 eV, leading to strong inversion in the c-Si. Such high offsets cannot be achieved in practice with a-Si:H,³⁴ but might be possible with other materials.

Other approaches are needed if lateral transport is to be conducted through the inversion layer. One possible improvement is to use p-type wafers. Electrons in c-Si have approximately three times higher mobility than holes, which would decrease the sheet resistance of the inversion layer. Another possibility to create the inversion layer under the active cell area is to use the metal-insulator-semiconductor approach with novel organic materials.³⁷

IV. CONCLUSION

The contribution of the inversion layer to lateral transport of photogenerated holes was studied with numerical simulations and LBIC measurements. Heterojunction solar cells of different area (2×2 , 4×4 , and $6 \times 6 \text{ mm}^2$) with and

without front ITO layers were prepared. For the *TCO* series of cells, no variation with position is observed. Increasing ΔE_{vb} at the a-Si:H/c-Si interface increases the band bending in c-Si, resulting in an increased hole concentration under the surface of the c-Si. The increased concentration leads to a lower sheet resistance of the inversion layer, which facilitates photogenerated charge carrier collection from the middle of the cell. Based on the simulation results, the required low sheet resistance of the inversion layer cannot be achieved with the a-Si:H materials system, and the inversion layer cannot replace the ITO layer for a high-efficiency cell on an n-type c-Si substrate.

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